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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JUN 16 2005

Application No.: 10/501,431 **Confirmation No.:** 9438
Applicants: S. A. Garyainov, et al.
Filing Date: July 13, 2004
Priority Date: March 1, 2002
Title: Fluxless Assembly of Chip Size Semiconductor Packages
Group Art Unit: 2812
Examiner: Walter L. Lindsay, Jr.
Docket No.: 02PCTEZ1

Los Angeles, California
June 16, 2005

Via Facsimile to (703) 872-9306

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO ELECTION/RESTRICTION REQUIREMENT

Dear Sir:

In reply to the Election/Restriction requirement imposed by the Examiner in this application on 06/09/2005, the Applicant elects, *with traverse*, the invention of Group II, claims 1-14, for examination, and provisionally withdraws claims 15 and 16 without prejudice to their reintroduction at a later date.

The grounds for the traverse of this requirement are set forth in the Remarks/Arguments beginning on page 2 of this paper.